

501.40692X00

07-16-03

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): MIYAZAKI, et al

RECEIVED

Serial No.:

09/964,490

JUL 1 1 2003

Filed:

September 28, 2001

**Technology Center 2100** 

For:

FABRICATION METHOD OF SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

Group:

2812

Examiner:

TBD

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 AND 1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 July 9, 2003

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find copies of documents to be considered by the Examiner during examination of the above-identified application. Also enclosed are English translations of the respective documents. Further enclosed is a Form, substantially equivalent to Form PTO-1449, listing the enclosed documents.

This Information Disclosure Statement is being submitted prior to a first Office Action on the merits in the above-identified application. Accordingly, requirements of 37 CFR 1.97(b) are satisfied.

In any event, the following Statement is made under 37 CFR 1.97(e)(2):

No item of information contained in this Information

Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making

Docket No. 501.40692X00

reasonable inquiry, no item of information contained in this

Information Disclosure Statement was known to any

individual designated in 37 CFR 1.56(c), more than three

months prior to the filing of this Information Disclosure

Statement.

The documents, enclosed, listed on the enclosed Form, are not in English.

For satisfying requirements of 37 CFR 1.98(a)(3), English translations of the listed

documents are enclosed.

1.

In view of the foregoing, it is respectfully submitted that all applicable

requirements of 37 CFR § 1.97 and § 1.98 have been satisfied, in connection with

the documents listed on the enclosed Form. Accordingly, consideration of the listed

documents, upon examination of the above-identified application, is respectfully

requested.

Please charge any shortage in fees due in connection with the filing of this

paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case

No. 501.40692X00), and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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WIS/sjg

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## LIST OF ART CITED BY APPLICANT (PTO-1449) APPLICANT Miyazaki, et al. FILING DATE September 28, 2001 U.S. PATENT DOCUMENTS EXAMINER'S INITIALS PATENT DATE NO. NO. 1 00 074 04477

EXAMINER'S							En l	NG DATE
INITIALS	PATENT NO.	DATE	NAM	≣	CLASS	SUBCLASS	LIL	ING DATE
	4,684,971	8/4/87	Payne			D		VED
	5,376,483	12/27/94	Rolfson			n		VED
	5,378,585	1/3/95	Watanabe			113	<u>L 1 1</u>	2003
	5,389,474	2/14/95	Iguchi, et al.				1	
	5,418,092	5/23/95	Okamoto	·	Techn	ology C	enter 2100	
	5,556,724	9/17/96	Tarumoto, et al.	-				
	5,741,613	4/21/98	Moon et al.					
	5,989,760	11/23/99	Mangat, et al.					
	5,948,572	9/7/99	Liu, et al.					
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	DATE CONSIDERED							

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.